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Verified and Acknowledged Examiner's Signature <u>[Signature]</u> Initials <u>mtt</u>					
ADDRESS Jack V. Musgrove SKJERVEN MORRILL MacPHERSON LLP Suite 700 25 Metro Drive San Jose, CA 95110-1349					
TITLE Linear associative memory-based hardware architecture for fault tolerant ASIC/FPGA work-around					
FILING FEE RECEIVED 1186	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following;		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit _____		